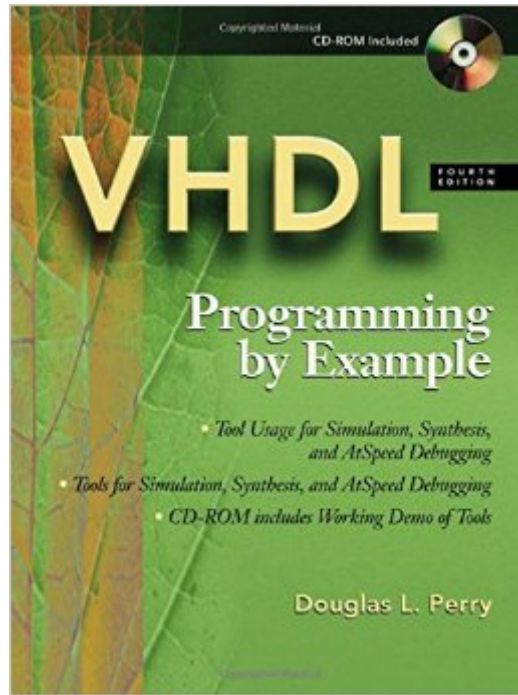The book was found

VHDL : Programming By Example

Synopsis

THE HANDS-DOWN FAVORITE USERâ ™S GUIDE TO VHDLCOMPLETELY UPDATED TO REFLECT THE VERY LATEST DESIGN METHODSCD-ROM WITH WORKING CODE EXAMPLES, VERIFICATION TOOLS AND MORENo matter what your current level of expertise, nothing will have you writing and verifying concise, efficient VHDL descriptions of hardware designs as fast â " or as painlessly â " as this classic tutorial from master teacher Doug Perry. Beginners will find it an invaluable learning tool and experienced pros will keep it on their desk as a trusted reference. Perry teaches VHDL through a series of hundreds of practical, detailed examples, gradually increasing in complexity until youâ ™re capable of designing a fully functional CPU. The new Fourth Edition has been completely updated with all of the VDHL codes used in the examples changed to reflect todayâ ™s faster and more efficient design methods. Youâ ™ll also find:* A CD-ROM containing working code of all of the VDHL examples, with their matching designs along with VITAL verification tools and a working copy of ModelSIM* All the tools you need for simulation and synthesis* A listing of the IEEE 1164 STD-LOGIC package used throughout the book* Useful tables and figures* Instructions for reading the Bachus-Naur format (BNF) descriptions found in the VHDL Language Reference Manual There truly is no faster or smarter way to master VHDL than Doug Perryâ ™s â œlearn by exampleâ • approach. It works!

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